



2133
Patent
700693-4011

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/878,554 Confirmation No.: 5168
Applicant : Xinghao Chen et al
Filing Date : June 11, 2001
Title : Efficiency of Fault Simulation By Logic Backtracking
Group Art Unit : 2133
Examiner : Joseph D. Torres
Docket No. : 700693-4011
Customer No. : 34313

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Technology Center 2100

RESPONSE TO OFFICE ACTION TRANSMITTAL

Commissioner for Patents
Mail-Stop Fee Amendment
P.O. Box 1450
Alexandria V/a 22313-1450

Dear Sir/Madam:

In response to the Office Action dated January 02, 2004, transmitted herewith is a Response for the above-identified application.

CERTIFICATE OF MAILING
(37 C.F.R. §1.8a)

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria V/a 22313-1450

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April 29, 2004
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Rita Hernandez
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Patent
Attorney Docket: 700693-4011



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RESPONSE TO OFFICE ACTION

Mail Stop: No-Fee Amendment
Commissioner for Patents
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Alexandria, VA 22313-1450

Sir:

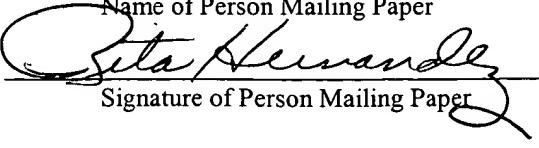
This paper is responsive to the Office Action mailed on January 2, 2004. Please amend the above-identified application as follows:

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